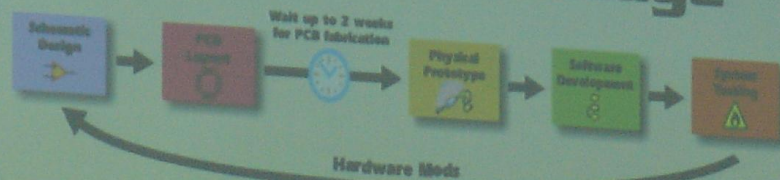


THE VIRTUAL SYSTEM MODELLING ADVANTAGE

- ▶ A HILS allows software to be developed and tested without waiting for all of the actual hardware to be built

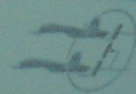
The VSM Advantage



With traditional design tools, software development and system testing cannot begin until a PCB and physical prototype are available - incurring a delay of up to 2-3 weeks. And if something is wrong with the hardware design, the whole process must be repeated.



Using Proteus VSM, software development can begin as soon as the schematic is drawn, and the combination of hardware and software can be thoroughly tested before physical prototyping.



VALAVIA
Independent Electronics R&D Lab

22 13:00