

THE VIRTUAL SYSTEM MODELLING ADVANTAGE

- A HLS allows software to be developed and tested without waiting for all of the actual hardware to be built

The VSM Advantage

With traditional design flow, software development and hardware testing cannot start until hardware is fabricated. This can take up to 12 weeks of hardware development and 12 weeks of hardware testing, for a total of 24 weeks of hardware development and testing.

Using Proteus VSM, software development and hardware testing can start simultaneously. This can reduce the total development and testing time to 12 weeks.



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