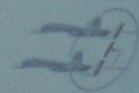
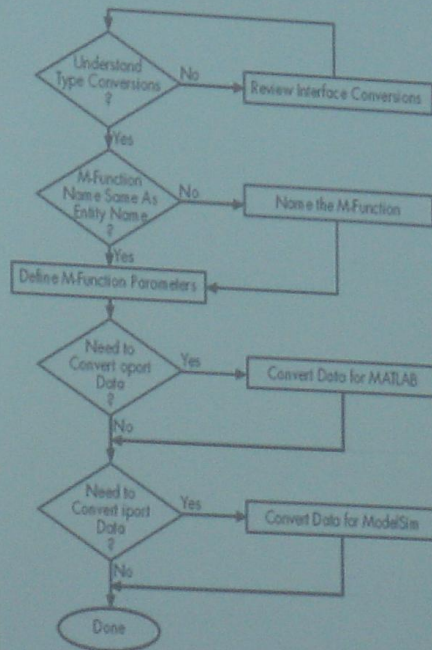


TEST MODULE. MW MATLAB AND NI LabVIEW SIT

Definition of algorithm for each test case at the modeling stage



VALAVIA
Independent Electronics R&D Lab

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