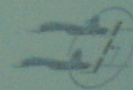
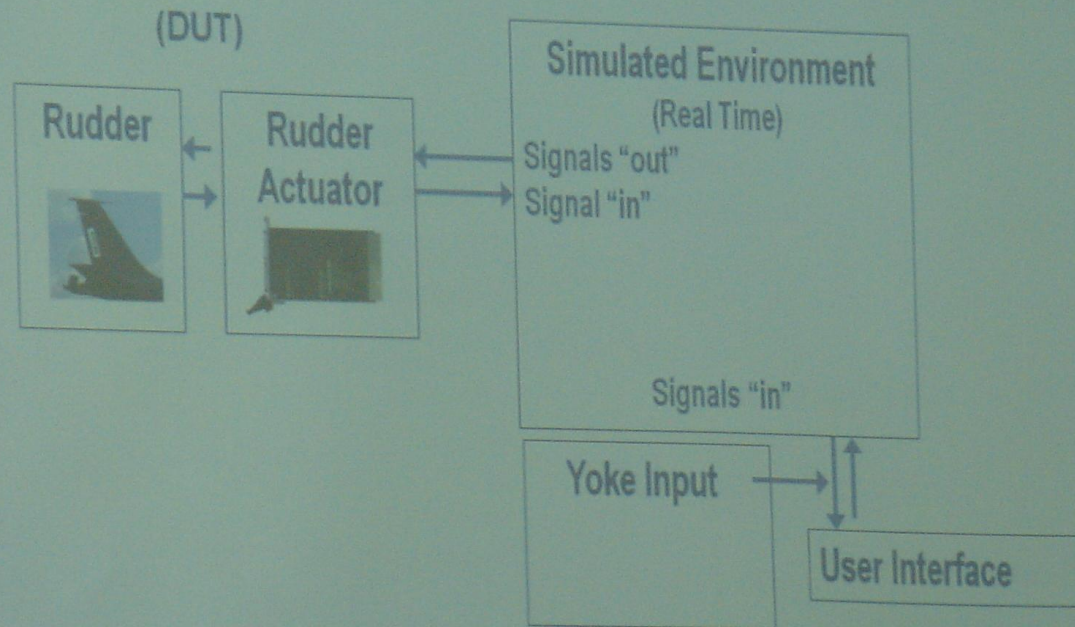


# ■ HARDWARE-IN-THE-LOOP SIMULATION

Test System Design



VALAVIA  
Independent Electronics RAD Lab

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